

Electronic Acknowledgement Receipt

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Application Number:	10643193
International Application Number:	
Confirmation Number:	3651
Title of Invention:	Circuits and methods for characterizing random variations in device characteristics in semiconductor integrated circuits
First Named Inventor/Applicant Name:	Azeez Bhavnagarwala
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Filer Authorized By:	
Attorney Docket Number:	YOR920030289US1 (8728-635
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Application Type:	Utility under 35 USC 111(a)

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Request for Continued Examination (RCE)	8728-635_rce.pdf	101127 614d6d322aa4743839499b7d53b01f00c0db2504	no	3

Warnings:

This is not a USPTO supplied RCE SB30 form.

Information:

2	Fee Worksheet (PTO-875)	fee-info.pdf	30454 078ba2ae09c33234fc10ed430d5b78df27a58183	no	2
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Warnings:**Information:**

Total Files Size (in bytes):			131581
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National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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